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45512 LAWRENCE CHO C/O Intellevate, LLC P. O. BOX 52050 MINNEAPOLIS. MN 55402 CONFIRMATION NO. 6538

FILING RECEIPT



Date Mailed: 04/17/2009

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections

Applicant(s)

Xiaodan Jiang, Shanghai, CHINA; Jinguan Dai, Shanghai, CHINA;

Power of Attorney: The patent practitioners associated with Customer Number 45512

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/CN2005/002232 12/17/2005

Foreign Applications

If Required, Foreign Filing License Granted: 04/11/2009

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US 10/585.680

Projected Publication Date: 07/30/2009

Non-Publication Request: No

Early Publication Request: No

Title

Method and apparatus for partitioning programs to balance memory latency

### Preliminary Class

711

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